

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: KIRA, ET AL

Art Unit: UNASSIGNED

Serial Number: NEW APPLICATION

Examiner: UNASSIGNED

Filed: HERewith

For: METHOD AND SYSTEM FOR FABRICATING A SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner  
of Patents and Trademarks  
Washington, D.C. 20231

February 24, 1995

Sir:

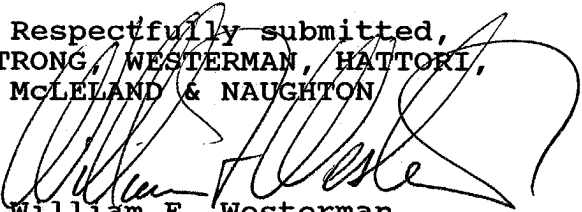
In compliance with 37 CFR 1.56, Applicant(s) call to the attention of the Patent and Trademark Office the references listed on the attached PTO-FB-A820.

Japanese Laid-Open Pat. Appl. No. 3-184352, Dated 8/12/1991  
Japanese Laid-Open Pat. Appl. No. 5-67648, Dated 3/19/1993

A copy of each of the references, and an English Abstract of each, is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2395.

Respectfully submitted,  
ARMSTRONG, WESTERMAN, HATTORI,  
MCLELAND & NAUGHTON

  
William F. Westerman  
Attorney for Applicant  
Reg. No. 29,988

Atty. Case No. 950107  
1725 K Street, N.W.  
Washington, D.C. 20006  
Tel: (202) 659-2930  
Fax: (202) 887-0357  
WFW/adt  
Enclosures: References (2)